

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	66	NMOS and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:14
L2	1	TCAM and NMOS and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:45
L3	51	"365"/\$.ccls. and NMOS and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:51
L4	7	"365"/\$.ccls. and (precharge with circuit) and NMOS and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:46
L5	2	"365"/\$.ccls. and NMOS and FET and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:50
L6	46	"365"/\$.ccls. and (NMOS with transistor) and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:56
L7	1	(CAM with (spare or redundan\$3)) and (precharge adj circuit) and (repair adj signal adj generator) and (switch\$3 adj circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:58
L8	1	(CAM with (spare or redundan\$3)) and (precharge adj3 circuit) and (repair adj3 signal adj3 generator) and (switch\$3 adj3 circuit)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:59
L9	1	(CAM with (spare or redundan\$3)) and precharge and (repair adj3 signal adj3 generator) and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:59

L10	1	CAM and (spare or redundan\$3) and precharge and (repair adj3 signal adj3 generator) and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:00
L11	123	CAM and (spare or redundan\$3) and precharge and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:00
L12	74	CAM and (spare or redundan\$3) and precharge and generator and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:00
L13	23	CAM and (spare or redundan\$3) and precharge and (signal adj3 generator) and switch\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:03
L14	18	CAM and (spare or redundan\$3) and (switch\$3 adj circuit) and (determin\$3 with defective)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 09:05
L15	19	365/49,200,185.23,185.26,203,191,204,230.06.ccls. and NMOS and (gate with power) and (source with ground) and (drain with ((search adj line) or SL or searchline))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 10:29
L16	2	"2002260389"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 10:47
L17	15	("5,319590" "5,949696" "6,320777" "6,522562" "6,411538" "6,445628" "6,496399").pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 10:51
L18	2	"20020015348"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 10:52
S1	5	(TCAM with (memory adj2 cell\$1)) same (compar\$5 with (NMOS near2 transistor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/28 08:43

S2	13	("4646271" "4780845" "5490102" "5495382" "6044055" "6108227" "6154384" "6496399" "6539455" "6542392" "6646900" "6665202" "6678184").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:37
S3	4	("4,646271" "4,780845" "5, 490102" "5,495382").pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:39
S4	7	("6,411538" "6,445628" "6, 496399" "5,319590" "5,949696" "6,320777" "6,522562").pn.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:40
S5	2	("6262907" "6288922").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:42
S6	219	gillingham.in.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:42
S7	58	gillingham.in. and "365"/\$.ccls.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 14:43
S8	1	(TCAM with (two adj2 memory adj2 cell\$1)) same (compar\$5 with (NMOS near2 transistor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 14:45
S9	1	(TCAM with (two adj2 memory adj2 cell\$1)) and (compar\$5 with (NMOS near2 transistor\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 14:45
S10	1	(TCAM with (two adj2 memory adj2 cell\$1)) same (NMOS near2 transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 14:46
S11	6	(TCAM with (memory adj2 cell\$1)) same (NMOS near2 transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 14:47
S12	1	(TCAM with (two adj2 memory adj2 cell\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 14:48

S13	28	(TCAM with (memory adj2 cell\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 15:53
S14	25	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) with (two with memory with cell\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 16:49
S15	5	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) with (two with memory with cell\$1)) same compar\$4 same NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 16:33
S16	10	("3229255" "5440715" "6108227" "6137707" "6154384" "6237061" "6411538" "6480406" "6496399").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 16:26
S17	5	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) with (two with memory with cell\$1)) and compar\$4 same NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 18:57
S18	6	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) with (two with memory with cell\$1)) and compar\$4 and NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 16:33
S19	6	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) same (two with memory with cell\$1)) and compar\$4 and NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 16:34
S20	26	((TCAM or (ternary adj2 content adj2 addressable adj2 memory)) same (memory with cell\$1)) and compar\$4 and NMOS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 07:00
S21	21	("20020093347" "4780845" "4791606" "5111427" "5319589" "5642320" "5724296" "6154384" "6166938" "6188594" "6240003" "6310880" "6320777" "6331961" "6400593" "6400594" "6418042" "6421265" "6430073" "6442090" "6597594").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 16:36

S22	13	("4646271" "4780845" "5490102" "5495382" "6044055" "6108227" "6154384" "6496399" "6539455" "6542392" "6646900" "6665202" "6678184").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/09 16:39
S23	2	"6,748484".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/09 16:52
S24	2	"5,319590".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 05:46
S25	2	"6,445628".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 05:46
S26	6	("3633175" "5046046" "5319589" "5644699" "5848074" "6275406").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 05:47
S27	5	("3701980" "4965767" "4975873" "4991136" "5130945").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 06:10
S28	51	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (compar\$4 adj2 circuit) and transistor\$1	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 07:02
S29	10	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (compar\$4 adj2 circuit) and transistor\$1 and (connect\$3 with (memory adj2 cell\$1) with (ML or (match adj2 line)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 07:35
S30	13	("4646271" "4780845" "5490102" "5495382" "6044055" "6108227" "6154384" "6496399" "6539455" "6542392" "6646900" "6665202" "6678184").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:13
S31	5	("5319589" "5319590" "5642114" "6108227" "6154384").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:21

S32	14	("5226005" "5319590" "5396449" "5446685" "5517441" "5598115" "5642114" "5646878" "5699288" "5870324" "5940852" "5949696" "5993363" "6044005").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:27
S33	2	("5280447" "5564052").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:31
S34	10	("4845668" "4996666" "5010516" "5014195" "5072422" "5440715" "5517441" "5592407" "5949696" "6044005").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:33
S35	3	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (S25 or four) with transistor\$1) and (connect\$3 with (memory adj2 cell\$1) with (ML or (match adj2 line))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 08:06
S36	39	("5327372" "5446685" "5517441" "5598115" "5706224" "5852569" "5859791" "5936873" "5964857" "5978246" "6044005" "6101115" "6101116" "6125049" "6128207" "6147891" "6166939" "6175514" "6191969" "6191970" "6240001" "6243280" "6256216" "6262907" "6262929" "6324087" "6341079" "6343029" "6349049" "6362990" "6362993" "6370613" "6373737" "6373738" "6389506" "6430074" "6452822" "6499081" "6502163").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 07:41
S38	18	("4110841" "4412314" "4653030" "4688196" "4833643" "4970689" "4991136" "5111427" "5319590" "5598115" "5703803" "5724296" "5757693" "5761114" "5909400" "5949696" "5999435" "6188594").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:02

S39	8	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (S25 or four) with transistor\$1) and (memory adj2 cell\$1) and (ML or (match adj2 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 08:36
S40	10	("3229255" "5440715" "6108227" "6137707" "6154384" "6237061" "6411538" "6480406" "6496399").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:14
S41	8	("5258946" "5469378" "6044005" "6101115" "6154384" "6157558" "6195278" "6262907").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:15
S42	8	("5258946" "5469378" "6044005" "6101115" "6154384" "6157558" "6195278" "6262907").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:20
S44	11	("3599205" "3671959" "3706977" "3760277" "3798544" "4231023" "4296475" "4809224" "4910750" "5432735" "5498980").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:27
S46	8	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (S25 or four) with transistor\$1) and (ML or (match adj2 line))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 08:36
S47	9	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (S25 or four) with transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/03/10 08:38
S48	8	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (S25 or four) with transistor\$1) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 13:58
S49	4	("4538243" "5034919" "5327372" "5351208").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/03/10 08:42
S50	9	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (4-transistor or (four with transistor\$1))) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:08

S51	15	("20020126519" "4813002" "4862412" "5195278" "5495382" "5568415" "5699288" "6108227" "6154384" "6188594" "6219271" "6320777" "6333254" "6339241" "6512684").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/26 14:02
S52	0	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (4-transistor or (four with transistor\$1))) and (2-inverter or (S51 with inverter))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:04
S53	2	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (4-transistor or (four with transistor\$1))) and (2-inverter\$1 or (two with inverters))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:06
S54	3	"2005006343"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:07
S55	11	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (4-transistor or (four with transistor\$1) or (two with inverters))) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:09
S56	11	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and ((compar\$4 adj2 circuit) with (4-transistor\$1 or (four with transistor\$1) or (two with inverters))) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:15
S57	15	("20020126519" "4813002" "4862412" "5195278" "5495382" "5568415" "5699288" "6108227" "6154384" "6188594" "6219271" "6320777" "6333254" "6339241" "6512684").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/26 14:11
S58	6	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (two adj memory adj cells) and ((compar\$4 adj2 circuit) and (4-transistor\$1 or (four with transistor\$1) or (two with inverters))) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:16

S59	6	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (two adj memory adj cells) and (compar\$4 adj2 circuit) and (4-transistor\$1 or (four with transistor\$1) or (two with inverters)) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:22
S60	10	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (two adj memory adj cells) and (4-transistor\$1 or (four with transistor\$1) or (two with inverters)) and match and search	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:22
S61	11	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (two adj memory adj cells) and (4-transistor\$1 or (four with transistor\$1) or (two with inverters))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/26 14:23
S62	2	"20020015348"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 10:42
S63	0	"10626729"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 10:42
S64	1	"10/626729"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 10:42
S65	2	10/279838	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 18:57
S66	1	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) same (repair adj3 signal adj3 generator) same indicat\$3 same defective	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 18:59
S67	1	(TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (repair adj3 signal adj3 generator) and indicat\$3 and defective	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 18:59

S68	7	(repair adj3 signal adj3 generator) and indicat\$3 and defective	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 20:50
S69	79	365/49,200,185.23,185.26,203, 191,204,230.06.ccls. and (TCAM or (ternary adj2 content adj2 addressable adj2 memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 20:56
S70	23	365/49,200,185.23,185.26,203, 191,204,230.06.ccls. and (TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (NMOS with transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 21:14
S71	15	("5226005" "5233559" "5289403" "5341500" "5371697" "5422838" "5905680" "6078987" "6122710" "6208543" "6362993" "6396749" "6411538" "6564344" "6721202").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2005/04/27 21:10
S72	27	"365"/\$.ccls. and (TCAM or (ternary adj2 content adj2 addressable adj2 memory)) and (NMOS with transistor\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/27 21:14